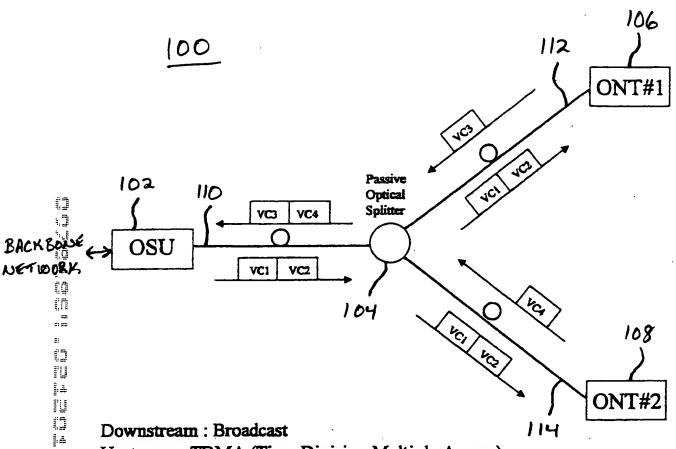
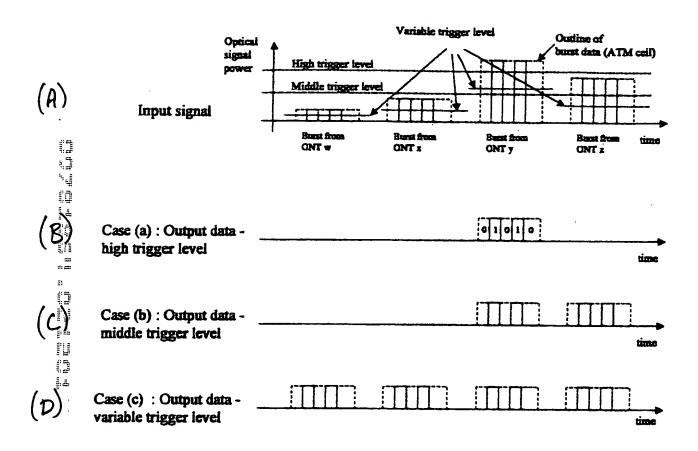
FIG. 1 (PRIOR ART)

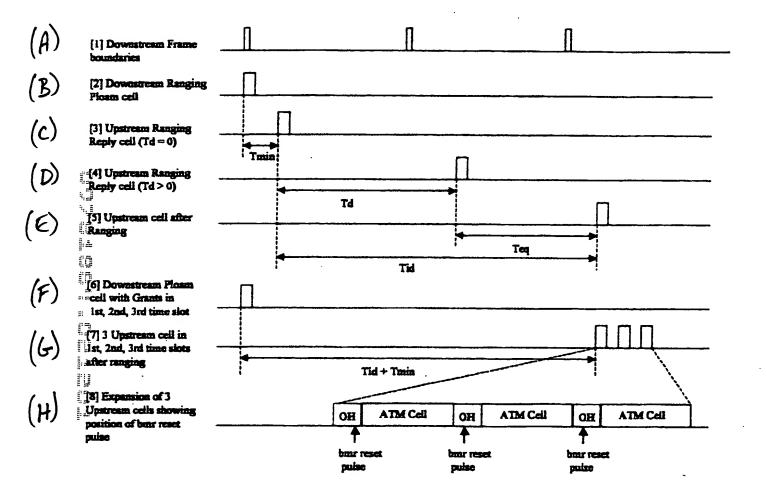


Upstream: TDMA (Time Division Multiple Access)

FIG. 2 (PRIOR ART)

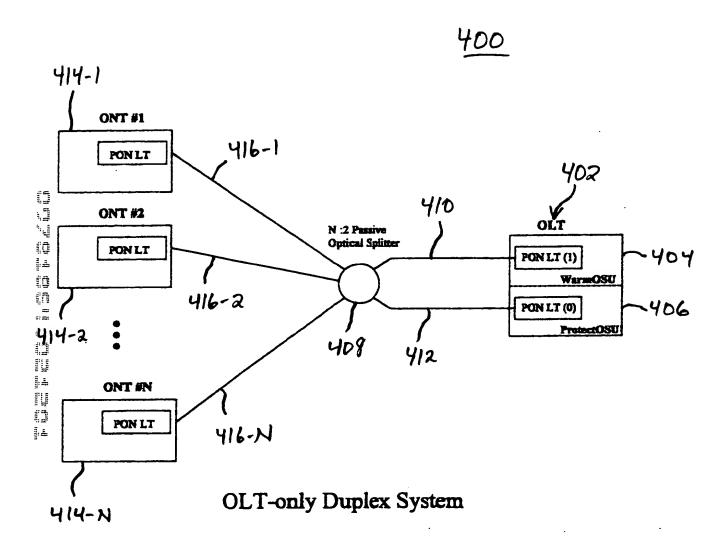


F16.3

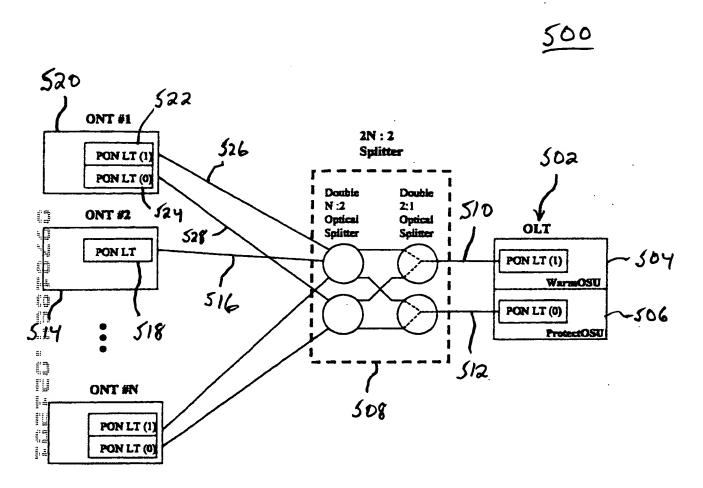


. 2

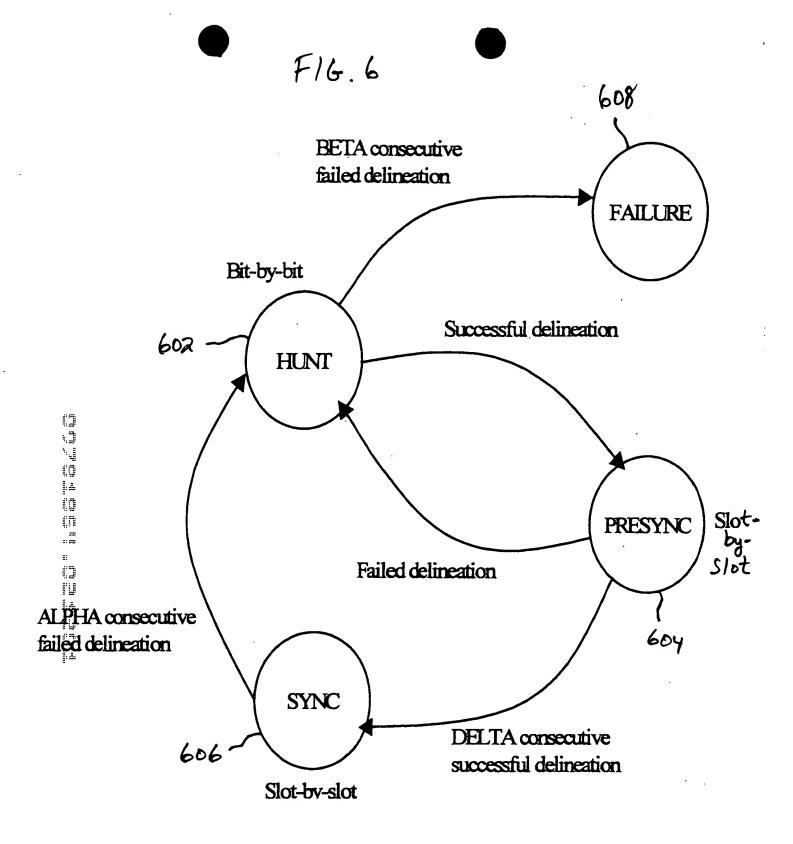
F16.4



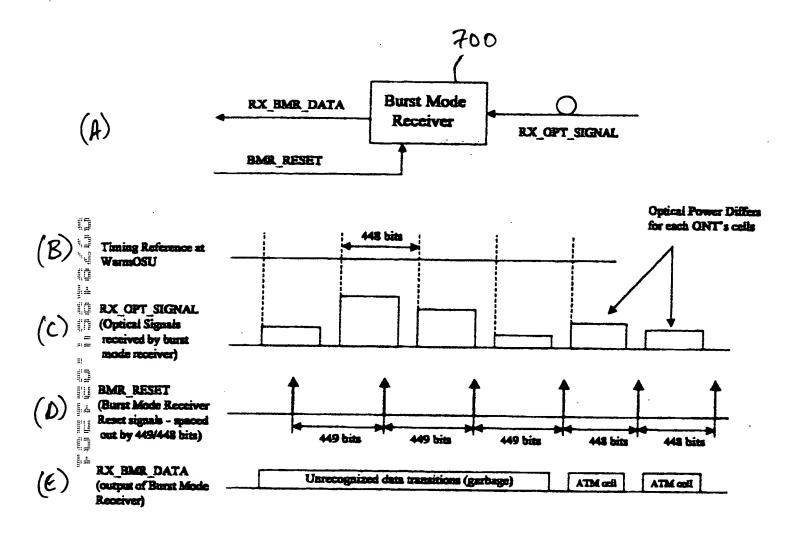
F16.5



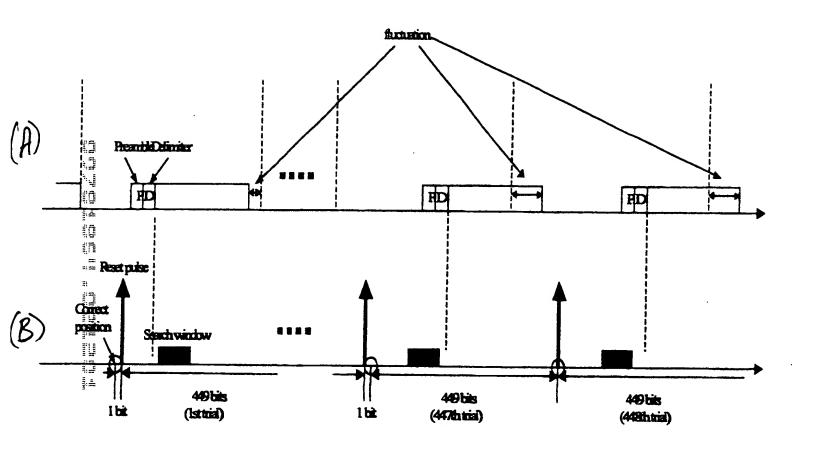
Partial Duplex System



F16.7



F16.8



F16.9

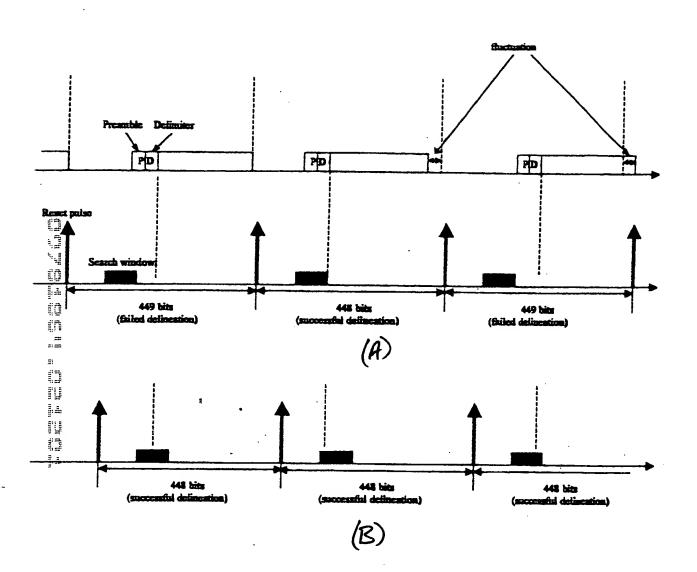
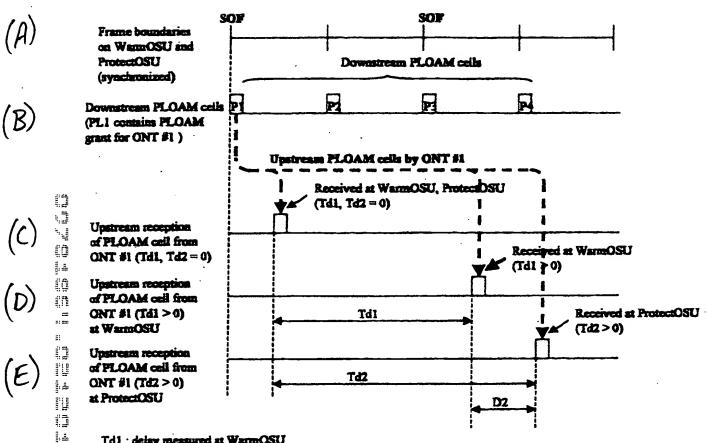


FIG. 10.

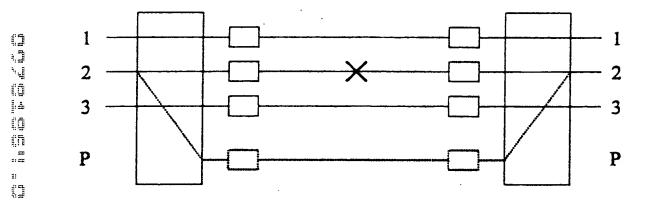


Td1: delay measured at WarmOSU
Td2: delay measured at ProtectOSU

D2: time difference between start of frame in WarmOSU and ProtectOSU, due to difference in distance to splitter

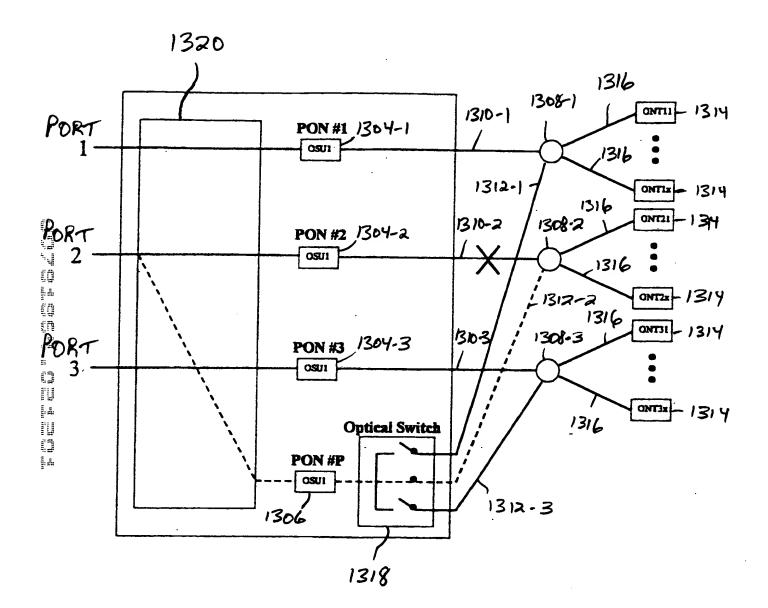
*SOF : Start of Frame.

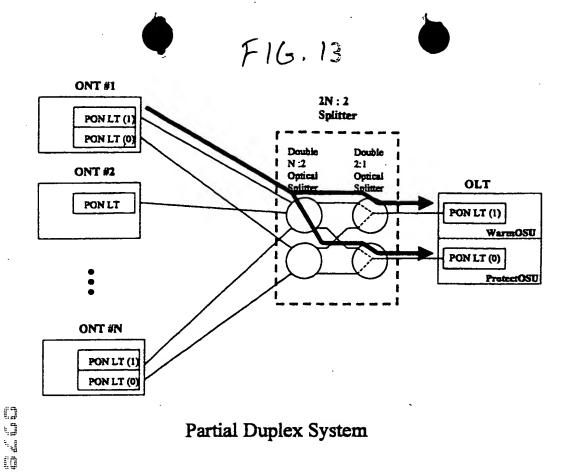
F16.11



1:N Protection (example N = 3)

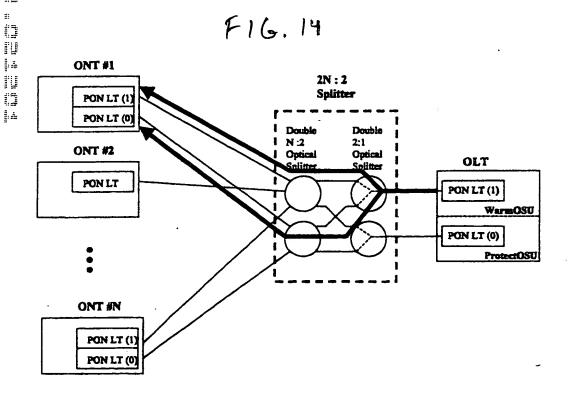
F16.12





Partial Duplex System

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Partial Duplex System